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 of this overhead was due to kernel traps, context switches, and receive interrupt handling. 15 The RPC time, the major bottlenecks were the slow network fabrics and the presence of the OS in the critical path and zero-copy transmission of arrays. All objects received are fully type-checked and can be directly used
simon.cs.cornell.edu/home/chichao/tr-1708.ps

Operating System Techniques for Distributed Multimedia - David Yau (Correct) (4 citations)
 control to user code without process context switching) are simpler to perform. A lightweight kernel two memory-to-memory data copies are made. The receive data path is similar, but in reverse. Data, that includes the concept of I/O efficient buffers for reduced copying, the concept of fast system
ftp.cs.utexas.edu/pub/lam/tr95-36.ps.Z

User Customization of Virtual Network Interfaces with U-Net/SLE - Oppenheimer, Welsh (1998) (Correct) (3 citations)

I/O bus transfer, and process or thread context switch. Another potential application is Another potential application is packet-specified receive buffers, in which the header of an incoming potential application is packet-specified receive buffers, in which the header of an incoming packet
www.cs.berkeley.edu/~mdw/projects/unet/./unet-sle/unet-sle-tr.ps.gz

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morse.uml.edu/~bkim/research/pprca_forum.ps.gz

RT-IPC: An IPC Extension for Real-Time Mach - Takuro Kitayama (1993) (Correct) (10 citations)
 usually, messages are delivered from a sender to a receiver without queueing, i.e. the average queue Message Message Message Message Free Message Buffer Message Queue Receiver Thread Queue Aaaaa Aaaaa
 it is very rare that two or more messages are queued in one message queue, usually, messages are
www.cs.cmu.edu/afs/cs/project/rtmach/public/papers/ipc93.ps

Virtual lines, a deadlock free and real-time routing mechanism.. - Gerard Smit (Correct)
 and buffer allocation mechanism for an ATM switching fabric. Since the fabric will be used to buffer allocation mechanism for an ATM switching fabric. Since the fabric will be used to transfer lines, i.e. a connection between sender and receiver. Virtual networks, implementing a number of
www.pegasus.esprit.ec.org/papers/paper93-05.ps

Flexible User-level Network Interface based on Embedded Processors - Hoe (1995) (Correct)
 of ATM (Asynchronous Transfer Mode) network, switched Ethernet and various other "switched" networks Interface Unit) design. 2.3 Arctic Switch Fabric FUNi2 will leverage on the interconnection capability. The user message interface (send and receive message queues) and network buffers are located
ftp.lcs.mit.edu/student-workshop/1995/abstracts/Hoe.ps

Models for Asynchronous Message Handling - Langendoen, Bhoedjang, Bal (1997) (Correct) (5 citations)
 unblocking a thread, involves a thread switch from the interrupted thread to the unblocked processes usually do not know when they will receive a message. Even when messages arrive at FM multithread-safe, added multicasting, improved buffer management, and, most importantly, added
ftp.cs.vu.nl/pub/amoeba/orca_papers/ieee-concurrency97.ps.gz

An Atomic Model for Message-Passing - Liu, Aiello, Bhatt (1993) (Correct) (33 citations)

one synchronous time step each processor can **receive** one atomic **message**, perform local computation, of blocking instructions is that no system **buffering** is required. However, the delay in waiting for
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<ftp.cpc.wmin.ac.uk/pub/HPCTI/P3/appendix-B2.ps.gz>

Modeling ATM Networks in a Parallel Simulation.. - Gburzynski.. (1995) (Correct) (1 citation)
 ATM is a connection-oriented packet-based **switching** technology designed for high-speed networks. hardware components like **buffers**, links and **switch fabrics**, and a model of ATM signaling, which constitutes a lower time stamp than the LVT (a straggler) is **received**. This forces the affected process to roll back
www.cs.ualberta.ca/~pawel/PAPERS/atmps.ps

On Using Intelligent Network Interface Cards to.. - Fiuczynski, Martin... (1998) (Correct) (4 citations)
 for Multimedia Conferencing Across Packet-Switched Networks. Computer Networks and ISDN Systems. range from packet filtering (e.g. Lazy **Receive Processing** [2] cluster based storage from the network directly to the region of frame **buffer** memory representing the applications window. As
www.cs.berkeley.edu/~rmartin/papers/mef-nossdav98.ps

Experiences of building ATM switches for the Local Area - Richard Black (Correct)
 Experiences of building ATM **switches** for the Local Area Richard Black, Ian Leslie, British Telecom. 2 Components 2.1 The **Switching Fabric** The Fairisle **switch fabric** is composed of 4 by 4
<ftp.cl.cam.ac.uk/public/papers/reports/ATM/docs-94-3/09sigcomm94.ps>

An Improved EFCI Scheme with Early Congestion Detection - Zhao, Li, Sigarto (1996) (Correct)
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Real-Time Communication in FDDI Networks - Malcolm, Kamat, Zhao (1995) (Correct) (4 citations)
message deadlines will be met in wide area packet **switched** networks [7, 11, 13, 18, 22, 25, 26, 27]An .6.2 B r i The size (number of bits) of **receive buffer** for stream S i .2.2 B s i The size occur, either due to missing deadlines or due to **buffer** overflow. These tests are extremely useful in the
www.cs.tamu.edu/research/realtime/malcolm-jrts-96.ps.gz

The Odd-Even ATM Switch - Koliass, KLEINROCK (Correct)
 TRANS. COMMUN. VOL. NO. 1 The Odd-Even ATM **Switch** y Christos KOLIAS yy and Leonard KLEINROCK
millennium.cs.ucla.edu/LK/Bib/PS/paper212.ps

Performance of ATM Switch Fabrics Using Cross-Point Buffers - Zhou, Atiquzzaman (1995) (Correct)
 Performance of ATM **Switch Fabrics** Using Cross-Point **Buffers** Bin Zhou and Performance of ATM **Switch Fabrics** Using Cross-Point **Buffers** Bin Zhou and M. are forwarded from a **buffer** during phase 1 and **received** during phase 2 of a cycle. The state of a
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Message-Passing Performance of Various Computers - Dongarra, Dunigan (1995) (Correct) (40 citations)
 affect performance. For small **messages**, context-switch times may contribute to delays. Touching all the to reduce **message** copies, for example, posting the **receive** before the send. Second order effects of **message** length. The receiving process usually provides a **buffer**, a maximum length, and the senders address. The
www.netlib.org/utk/papers/latbw.ps

Parallel Simulation of Data parallel Programs - Sundeep Prakash (1995) (Correct)
 and synchronization over the HPS (high performance **switch**) The resulting program executes as one pattern is one in which every processor **receives** a deterministic set of **messages** (unchanged over statement, a **message** is deposited in the **receive buffer** of the destination process. brevc mtype(
pcl.cs.ucla.edu/pub/papers/wlpc95-parallel.ps.gz

On-line Avoidance of the Intrusive Effects of Monitoring on.. - Waning Wu (1996) (Correct) (1 citation)
before **receive**. arrives after **receive**. Figure 8: **Switching** vs Continued Execution. Conversely, consider
of a **message** by a process is deterministic if the **receive** identifies a unique sender and non-deterministic
Processes communicate via **message** passing and a **queue** of pending **messages** is maintained for each
www.cs.pitt.edu/~gupta/research/Dist/icdcs96a.ps

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[Minimization of Communication Cost Through Caching in.. - Sistla, Wolfson, Huang \(1998\) \(Correct\) \(3 citations\)](#)

In other words, the mobile user subscribes to **receive** all the updates of x. This way the reads access will soon have online access to a large number of **databases** via wireless networks. Because of limited www.eecs.uic.edu/~wolfson/html/./mobile_ps/tpds.ps

[Effectiveness of Message Strip-Mining for Regular and.. - Akiyoshi Wakatani \(1994\) \(Correct\) \(4 citations\)](#)

strip-mining 3.1 Configuration Suppose that the **received data** of the communication is used in the loop the specific destination processor into a **message buffer**. The time for the preprocessing is in proportion implement parallel algorithms by distributing large **data** structures across a multicomputer system. To hide www.cse.ogi.edu/Sparse/paper/wakatani.pdcs.94.ps

[A Comparative Study of Fuzzy Versus "Fixed" Thresholds for.. - Bonde, Jr., Ghosh \(1994\) \(Correct\) \(1 citation\)](#)

Thresholds for Robust **Queue** Management in Cell-Switching Networks Allen R. Bonde, Jr. GTE Government smoothing functions, and the use of finite-sized **buffers** with **queue** management techniques. Most **queue** enws458.eas.asu.edu/Pub/bonde_1d.ps

[Problems Encountered in the Machine-assisted Proof of Hardware - Paul Curzon \(1994\) \(Correct\) \(3 citations\)](#)

the Fairisle Asynchronous Transfer Mode (ATM) **switching fabrics** [7]Fairisle is an existing network, Asynchronous Transfer Mode (ATM) **switching fabrics** [7]Fairisle is an existing network, designed issues of ATM networks, and carries real user **data**. The **switching fabrics** that we considered contain www.cl.cam.ac.uk/Research/HVG/atmproof/PAPERS/charme95.ps.gz

[Innovative Networking Concepts Tested On The.. - Friedman, Gupta.. \(Correct\)](#)

link, using a prototype Frame Relay Access Switch (FRACS) developed for the CSHCN by COMSAT motivated by the commercial driver of low-cost **receive-only** satellite terminals that can operate in a background traffic for Ethernet access and gateway **buffering** was studied. RESULTS Measurements and www.glue.umd.edu/~danielf/albuq95.ps.gz

[Multihop Networks: Performance Modeling Under Non-Uniform.. - Noel, Tang \(Correct\)](#)

A. Network Model .Node n **Switching fabric** Traffic extractor 1 2 1 d n n d Local Model .Node n **Switching fabric** Traffic extractor 1 2 1 d n n d Local Station 2 for wavelength assignments of transmitters and **receivers** at a node whereas in the latter, multihop www.ee.sunysb.edu/~wtang/ipccc.ps

[Performance Of Atm/oc-12 On The Intel Paragon - Dunigan \(Correct\)](#)

Tests With Oc12 Circuits Going Through A Fore Atm **Switch**. The **Switch** Added 3 s To The Latency And Had No layer requirements. The hardware interface has **receive** and transmit **buffers**, SAR logic, TCP/IP The hardware interface has **receive** and transmit **buffers**, SAR logic, TCP/IP acceleration logic, and logic www.epm.ornl.gov/~dunigan/atmoc12.ps

[A Performance Comparison of Buffering Schemes for Multistage.. - Bin Zhou \(1995\) \(Correct\) \(2 citations\)](#)

Comparison of **Buffering** Schemes for Multistage **Switches** Bin Zhou M. Atiquzzaman Dept. of Comp. Science systems. MINs have also been proposed as **switching fabrics** in ATM networks in the future Broadband ISDN for each output link [1]a **buffer** must be able to **receive** up to d packets at a time, where d is the size www.engr.udayton.edu/faculty/matiquzz/papers/bin-ica3pp-buf-cam.ps

[A Comprehensive Analytical Model for Wormhole Routing in.. - Draper, Ghosh \(1994\) \(Correct\) \(9 citations\)](#)

Cosmic Cube [20]used "store-and-forward" packet-switching methods for routing **messages** among the of the RE are used by the node to inject and **receive**, respectively, **messages** to/from the network. The have a 4-flit capacity [7]While only one flit **buffer** per channel is needed to implement wormhole

ftp.lans.ece.utexas.edu/pub/pproc/worm_jpdc94.ps.Z

Volume Models for Volumetric Data - Ranjan, Fournier (1994) (Correct) (6 citations)

the role of the outside and the inside could be **switched**. The problem is then to represent and visualize and display an iso-surface defined by some **threshold** value. In this paper we describe a method to Volume Models for Volumetric Data Vishwa Ranjan and Alain Fournier Department of www.cs.ubc.ca/labs/imager/tr/ps/ranjan.1993a.ps.gz

On the Advantage of Being the First Server - Refael Hassin (Correct)

road. A driver who needs to fill his tank sees the **queue** situation at the first station but not at the and answer the question. Key words: **Queues**, **threshold** strategies. 1 Introduction In a common a producer (or a seller) does not provide specific **data** on the good manufactured (or sold) by him. www.math.tau.ac.il/~hassin/Q.ps.gz

DRMA with Multiple Slots Reservation and.. - Komoriya.. (2000) (Correct)

Model For voice terminals, each conversation is **switched** between talking and silent states byaspeech that the **data** terminal has the infinite long **buffer** to keep **data messages** whichhave not transmitted the spectrum efficiency of the integrated voice and **data** services. In that protocol, although the fixed www.sasase.ics.keio.ac.jp/list/conference/.../helsinki/00/2000conf/yota_vtc.pdf

The interaction of the TCP flow control procedure in end .. - Wechta, Eberlein.. (1998) (Correct) (1 citation)

flow control mechanism for use in IEEE 802.3 **switches** J. Wechta, A. Eberlein, F. Halsall Department www.enel.ucalgary.ca/People/eberlein/Publications/hpn98.ps.gz

Using PVM 3.0 to Run Grand Challenge Applications on.. - Dongarra, Geist.. (1992) (Correct)

buffers, process signalling, and user definable **receive** contexts. In this paper we will focus on only dynamic process groups, multiple **message buffers**, process signalling, and user definable **receive** different architectures, operating systems, and **data** formats to cooperate. PVM (Parallel Virtual ftp.netlib.org/ncwn/siam93-pvmgc.ps

Real-Time Scheduling of Switching Nodes Based on Asynchronous.. - Jay Hyman (1990) (Correct) (2 citations)

Real-Time Scheduling of **Switching** Nodes Based on Asynchronous Time Sharing Jay consists of three elements: Input **Buffers**, **Switch Fabric** and Output **Buffers**. The fundamental requirement links. It consists of three elements: Input **Buffers**, **Switch Fabric** and Output **Buffers**. The ftp.ctr.columbia.edu/CTR-Research/comet/public/papers/90/HYM90.ps.gz

Random Early Detection Gateways for Congestion Avoidance - Floyd, Van Jacobson (1993) (Correct) (998 citations)

K.Congestion Control for High Speed Packet **Switched** Networks "INFOCOM '90, pp. 520-526, 1990. 3] Early Random Drop gateways, the misbehaving users **received** roughly 75% higher throughput than the users Source Quench **messages** to source hosts before the **buffer** space at the gateway reaches capacity [26]and ftp.ee.lbl.gov/papers/early.ps.gz

Active Virtual Network Management Protocol - Bush (1999) (Correct) (1 citation)

Message Vm Virtual Message T2 T10 T5 T Host Switch Rm Router Switch Router Lp Pp Pp Lp Lp Pp Dp Dp in Section 2.2. A Logical Process contains a **Receive Queue** (QR)Send **Queue** (QS)and **State Queue** Protocol caches predicted values within a **State Queue** and makes them available to a standard network www.crd.ge.com/people/bush/an/pads99.ps

The Performance Impact of Flexibility in the Stanford FLASH.. - Heinrich (1994) (Correct) (39 citations)

transfer logic places the **message data** into a **data buffer**, a cache line-sized (128-byte) on-chip storage header is first stored in an incoming **queue**. The first stage in the macropipeline, the inbox, proposed. We believe that the insights from these **data** highlight the potential bottlenecks in scalable www.eecg.toronto.edu/~tcm/other_papers/flash_asplos94.ps.Z

VIA over the CLAN Network - Riddoch, Pope, Mansley (2000) (Correct) (2 citations)

model has since been adopted for the Infiniband **switched fabric** interconnect, which has wide and powerful has since been adopted for the Infiniband **switched fabric** interconnect, which has wide and powerful In A Remote Process. The Vi Has A Send **Queue** And A **Receive Vi Cq Device Driver Vi Vi Interface System** www.lce.eng.cam.ac.uk/~djr23/pubs/tr.lce.01.2.pdf

[Compile/Run-time Support for Threaded MPI Execution on.. - Tang, Shen, Yang \(1999\) \(Correct\) \(3 citations\)](#)
disadvantages for MPI jobs because process context **switch** and synchronization are expensive. Secondly,
and allow concurrent access by a sender and a **receiver**. Our study is leveraged by previous research in
between two MPI nodes must go through the system **buffer** and **buffer** copying degrades the communication
www.cs.ucsb.edu/TRs/techreports/TRCS98-30.ps

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networks, and the same model is proposed for **Infiniband**. Existing implementations suffer from high model has since been adopted for the **Infiniband switched fabric** interconnect, which has wide and powerful has since been adopted for the **Infiniband switched fabric** interconnect, which has wide and powerful
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[Jiuxing Liu, Jiesheng Wu, Sushmitha P. Kini, Darius... - Ranjit Noronha Pete](#) (Correct)

MPI over **InfiniBand**: Early Experiences Jiuxing Liu, Jiesheng Wu, this industry standard is to use a scalable **switched fabric** to design the next generation clusters industry standard is to use a scalable **switched fabric** to design the next generation clusters and
www.cse.ohio-state.edu/~liuj/pub/liu_mvapich_tech.pdf

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www.cs.berkeley.edu/~mdw/projects/unet/.unet-sle/unet-sle-tr.ps.gz

[Implementing Efficient and Scalable Flow Control Schemes... - Infiniband Jiuxing Liu](#) (Correct)

and Scalable Flow Control Schemes in MPI over **InfiniBand** Jiuxing Liu Dhabaleswar K. Panda Computer and an InfiniScale MT43132 Eight 4x Port **InfiniBand Switch** [15]The HCA adapters work under the PCI-X nodes and I/O nodes are connected to the **fabric** by Channel Adapters (CA)Channel Adapters
nowlab.cis.ohio-state.edu/projects/mpi-iba/.publication/liu_cac04.pdf

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